



**Speedup your applications in
the cloud seamlessly**

- **Dr. Christoforos Kachris, co-founder**
CEO
- **Ioannis Stamelos, co-founder**
Software Development Director
- **Elias Koromilas, co-founder**
Hardware Development Director

Problem statement - Mission

- Data Scientist and programmers need to **run faster** their programs that process huge amounts of data.
- **InAccel** offers **accelerators** in the form of IP cores (circuits), that speedup their applications (machine learning, AI, data analytics, databases)
- InAccel offers the IP cores in the cloud with several advantages (no need to purchase expensive equipment, pay-as-you-go)



Flexibility versus High performance



CPU: High flexibility, low throughput



Chip Accelerators: High throughput, high NRE



Programmable Accelerators: High performance, high flexibility

Programmable accelerators (FPGAs) can provide massive parallelism and higher efficiency than CPUs for certain categories of applications

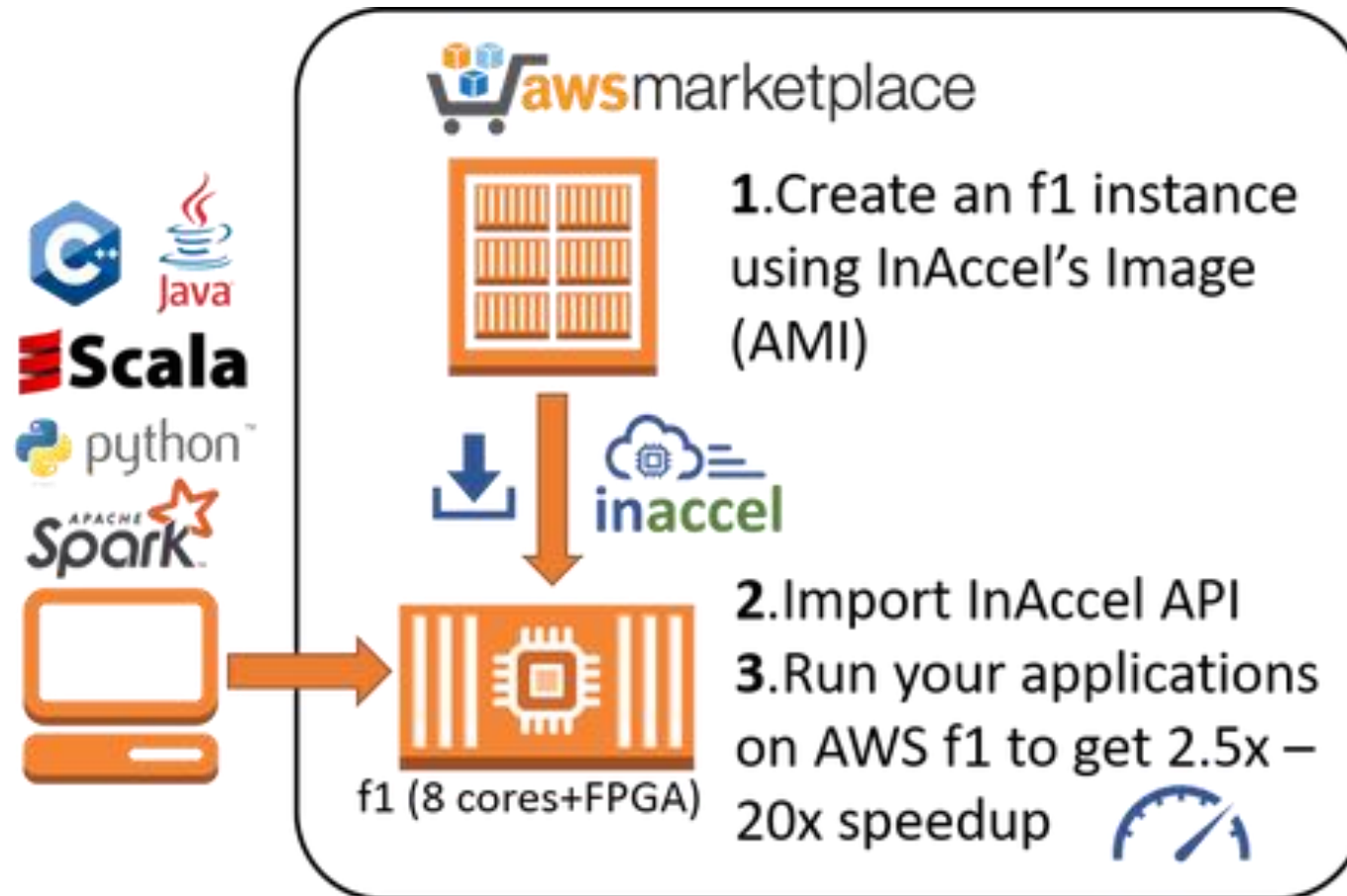
InAccel offers novel accelerators as IP cores to boost the performance of their applications (similar to specialized containers)

```
module filter1 (clock, rst, strm_in, strm_out)
integer i,j; //index for loops
always@(posedge clock)
for (i=0; i<NUMUNITS; i=i+1)
tmp_kernel[j] = k[i*OFFSETX];
```

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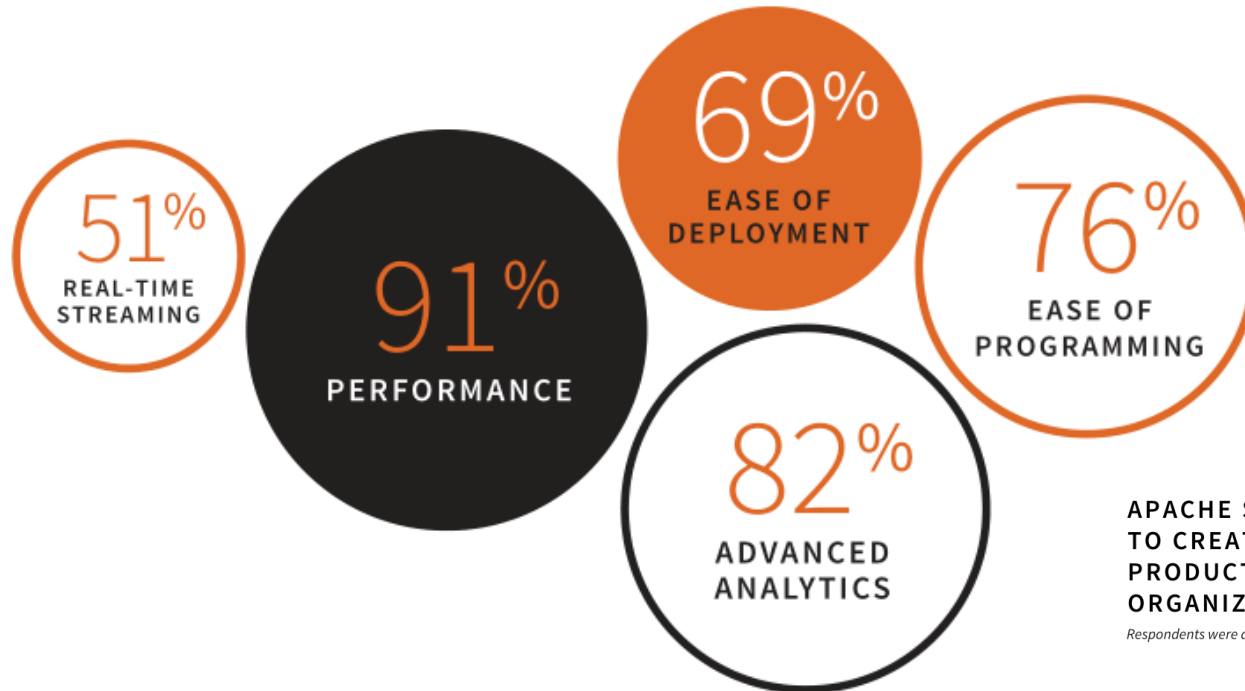
InAccel innovation

- Accelerators as building blocks (IPs) for widely used frameworks



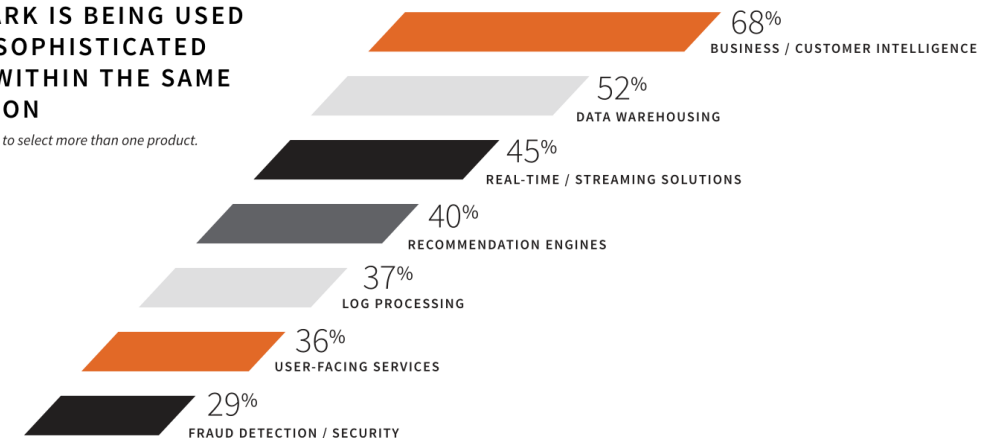
Main target

FEATURES USERS CONSIDER IMPORTANT



APACHE SPARK IS BEING USED TO CREATE SOPHISTICATED PRODUCTS WITHIN THE SAME ORGANIZATION

Respondents were allowed to select more than one product.



Strengths (advantages)

- **Scalable** to worldwide market



- **Lower cost** to advertise, communicate, promote it is on the Amazon AWS Store



- Be (among) the first to provide these IP cores connected **seamlessly** with Spark



Advantages:

- **Turn-key solutions**
- **Tailored-made solutions**



Opportunity: Amazon has released the FPGA platforms in the cloud. We are among the first to have early access to the development platforms.

- Participation in many Data Science Venues:

Strata
DATA CONFERENCE

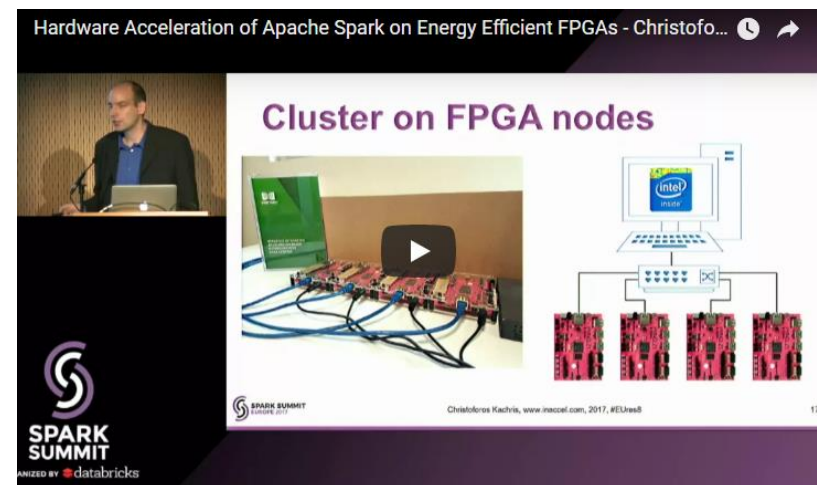


GSMA **MOBILE**
WORLD CONGRESS

BARCELONA 26 FEB-1 MAR 2018



SPARK+AI
SUMMIT 2018
JUNE 4 - 6 | SAN FRANCISCO
ORGANIZED BY  databricks



Collaborations

Accelerator Program



A quick start program to enable companies to accelerate products and services with FPGAs on the cloud. Target workloads include data analytics, genomics, video processing, machine learning, financial technology, security, and storage.



A Thriving Ecosystem

The QuickAlliance program brings together leading providers of FPGA Accelerators, IP and libraries, FPGA boards and platforms, and design services into one unified ecosystem where out-of-the-box user experience and ease of design are paramount. By delivering their products within QuickStore, QuickAlliance members lower their cost of sale and support while exposing their products to a whole new population of developers.

Advantages of Joining Invent ICT

A blue-outlined cloud shape containing the text "New mindset".

New mindset

A blue-outlined cloud shape containing the text "Support".

Support

A blue-outlined cloud shape containing the text "Mentors".

Mentors

A blue-outlined cloud shape containing the text "Exhibitions".

Exhibitions

Seed round fund announced recently

PRESS RELEASE

7/5/2018

InAccel raises \$600,000 seed round from Marathon Venture Capital to accelerate cloud workloads using FPGAs.

- SW on Intel Xeon

- Accel on FPGA MPSoC

Thank you!